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Bib Data Sheet		1. <u>2.</u> 1		·					
SERIAL NUMBE 09/697,305	ĒR	FILING DATE 10/27/2000 RULE _	CLASS 324	GROUP ART UNIT 2858			ATTORNEY DOCKET NO. YMOR:186		
APPLICANTS /									
Takaki Yoshida, Osaka, JAPAN;									
Reisuke Shimoda, Osaka, JAPAN;									
** CONTINUING DATA **********************************									
** FOREIGN APPLICATIONS ************************************									
JAPAN 11-307872 10/29/1999									
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/21/2000 -									
Foreign Priority claimed		yes on no	STATE OR	6	ETC	тоти	۸۱	INDEPENDENT	
35 USC 119 (a-d) conditions / Ino American COUNTRY DRAWING CLAIMS CLAIMS									
Verified and JAPAN 50 52 5									
Acknowledged	F/kar	Inder's Signature IIII	Mals						
PARKHURST & WENDEL, LLP									
1421 Prince Street, Suite 210 /									
Alexandria ,VA 22314									
TITLE									
Fault detecting method and layout method for semiconductor integrated circuit									
					All Fees				
						1.16 Fees (Filing)			
	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following:								
					☐ 1,17 Fees (Processing Ext. of time)				
					1.18 Fees (Issue)				
	16-033								
	70	00-1			U Other				
			•		☐ Credit				